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VLSI-SoC: Technology Advancement on SoC Design

29th IFIP WG 10.5/IEEE International Conference on Very Large Scale Integration, VLSI-SoC 2021 Singapore, October 4–8, 2021 Revised and Extended Selected Papers



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VLSI-SoC: From Systems to Silicon Ricardo Reis, Adam Osseiran, Hans-Joerg Pfleiderer, 2007-10-01 This book contains extended and revised versions of the best papers that were presented during the thirteenth edition of the IFIP TC 10 International Conference on Very Large Scale Integration a Global System on Chip Design and CAD conference This conference provides a forum to exchange ideas and show industrial and academic research results in the field of microelectronics design VLSI-SoC: From Algorithms to Circuits and System-on-Chip Design Andreas Burg, Ayse Coskun, Matthew Guthaus, Srinivas Katkoori, Ricardo Reis, 2013-11-26 This book contains extended and revised versions of the best papers presented at the 20th IFIP WG 10 5 IEEE International Conference on Very Large Scale Integration VLSI SoC 2012 held in Santa Cruz CA USA in October 2012 The 12 papers included in the book were carefully reviewed and selected from the 33 full papers presented at the conference The papers cover a wide range of topics in VLSI technology and advanced research They address the current trend toward increasing chip integration and technology process advancements bringing about stimulating new challenges both at the physical and system design levels as well as in the test of these <u>VLSI-SoC:</u> The Advanced Research for Systems on Chip Salvador Mir, Chi-Ying Tsui, Ricardo Reis, Oliver C.S. systems Choy, 2012-09-25 This book contains extended and revised versions of the best papers presented at the 19th IFIP WG 10 5 IEEE International Conference on Very Large Scale Integration VLSI SoC 2011 held in Hong Kong China in October 2011 The 10 papers included in the book were carefully reviewed and selected from the 45 full papers and 16 special session papers presented at the conference The papers cover a wide range of topics in VLSI technology and advanced research They address the current trend toward increasing chip integration and technology process advancements bringing about stimulating new challenges both at the physical and system design levels as well as in the test of theses systems

VLSI-SoC: Design Methodologies for SoC and SiP Christian Piguet, Ricardo Reis, Dimitrios Soudris, 2010-04-06 This book contains extended and revised versions of the best papers that were p sented during the 16th edition of the IFIP IEEE WG10 5 International Conference on Very Large Scale Integration a global System on a Chip Design CAD conference The 16th conference was held at the Grand Hotel of Rhodes Island Greece October 13 15 2008 Previous conferences have taken place in Edinburgh Trondheim V couver Munich Grenoble Tokyo Gramado Lisbon Montpellier Darmstadt Perth Nice and Atlanta VLSI SoC 2008 was the 16th in a series of international conferences sponsored by IFIP TC 10 Working Group 10 5 and IEEE CEDA that explores the state of the art and the new developments in the field of VLSI systems and their designs The purpose of the conference was to provide a forum to exchange ideas and to present industrial and research results in the fields of VLSI ULSI systems embedded systems and croelectronic design and test

VLSI-SoC: Research Trends in VLSI and Systems on Chip Giovanni De Micheli, Salvador Mir, Ricardo Reis, 2010-08-23 This book contains extended and revised versions of the best papers presented during the fourteenth IFIP TC 10 WG 10 5 International Conference on Very Large

Scale Integration This conference provides a forum to exchange ideas and show industrial and academic research results in microelectronics design The current trend toward increasing chip integration and technology process advancements brings about stimulating new challenges both at the physical and system design levels **VLSI-SoC:** New Technology Enabler Carolina Metzler, Pierre-Emmanuel Gaillardon, Giovanni De Micheli, Carlos Silva-Cardenas, Ricardo Reis, 2020-07-22 This book contains extended and revised versions of the best papers presented at the 27th IFIP WG 10 5 IEEE International Conference on Very Large Scale Integration VLSI SoC 2019 held in Cusco Peru in October 2019 The 15 full papers included in this volume were carefully reviewed and selected from the 28 papers out of 82 submissions presented at the conference The papers discuss the latest academic and industrial results and developments as well as future trends in the field of System on Chip SoC design considering the challenges of nano scale state of the art and emerging manufacturing technologies In particular they address cutting edge research fields like heterogeneous neuromorphic and brain inspired biologically inspired approximate computing systems VLSI-SoC: Technologies for Systems Integration Jürgen Becker, Marcelo De Oliveira Johann, Ricardo Reis, 2013-11-27 This book contains extended and revised versions of the best papers presented at the 17th IFIP WG 10 5 IEEE International Conference on Very Large Scale Integration VLSI SoC 2009 held in Florian polis Brazil in October 2009 The 8 papers included in the book together with two keynote talks were carefully reviewed and selected from 27 papers presented at the conference The papers cover a wide variety of excellence in VLSI technology and advanced research addressing the current trend toward increasing chip integration and technology process advancements bringing about stimulating new challenges both at the physical and system design levels as well as in the test of theses systems

Recent Advances in Intrusion Detection Somesh Jha, Robin Sommer, Christian Kreibich, 2010-09-02 This book constitutes the refereed proceedings of the 13th International Symposium on Recent Advances in Intrusion Detection RAID 2010 held in Ottawa Canada in September 2010 The 24 revised full papers presented together with 15 revised poster papers were carefully reviewed and selected from 102 submissions The papers are organized in topical sections on network protection high performance malware detection and defence evaluation forensics anomaly detection as well as web security

VLSI-SoC: Design Trends Andrea Calimera, Pierre-Emmanuel Gaillardon, Kunal Korgaonkar, Shahar Kvatinsky, Ricardo Reis, 2021-07-14 This book contains extended and revised versions of the best papers presented at the 28th IFIP WG 10 5 IEEE International Conference on Very Large Scale Integration VLSI SoC 2020 held in Salt Lake City UT USA in October 2020 The 16 full papers included in this volume were carefully reviewed and selected from the 38 papers out of 74 submissions presented at the conference The papers discuss the latest academic and industrial results and developments as well as future trends in the field of System on Chip SoC design considering the challenges of nano scale state of the art and emerging manufacturing technologies In particular they address cutting edge research fields like low power design of RF analog and mixed signal circuits EDA tools for the synthesis and verification of heterogenous SoCs accelerators for

cryptography and deep learning and on chip Interconnection system reliability and testing and integration of 3D ICs The VLSI-SoC: At the Crossroads of Emerging Trends Alex Orailoglu, H. Fatih Ugurdag, Luís conference was held virtually Miguel Silveira, Martin Margala, Ricardo Reis, 2015-11-28 This book contains extended and revised versions of the best papers presented at the 21st IFIP WG 10 5 IEEE International Conference on Very Large Scale Integration VLSI SoC 2013 held in Istanbul Turkey in October 2013 The 11 papers included in the book were carefully reviewed and selected from the 48 full papers presented at the conference An extended version of a previously unpublished high quality paper from VLSI SoC 2012 is also included The papers cover a wide range of topics in VLSI technology and advanced research They address the current trend toward increasing chip integration and technology process advancements bringing about stimulating new challenges both at the physical and system design levels as well as in the test of these systems VLSI-SoC: Design for Reliability, Security, and Low Power Youngsoo Shin, Chi Ying Tsui, Jae-Joon Kim, Kiyoung Choi, Ricardo Reis, 2016-09-12 This book contains extended and revised versions of the best papers presented at the 23rd IFIP WG 10 5 IEEE International Conference on Very Large Scale Integration VLSI SoC 2015 held in Daejeon Korea in October 2015 The 10 papers included in the book were carefully reviewed and selected from the 44 full papers presented at the conference The papers cover a wide range of topics in VLSI technology and advanced research They address the current trend toward increasing chip integration and technology process advancements bringing about new challenges both at the physical and system design levels as well as in VLSI-SoC: Technologies for Systems Integration Jürgen Becker, Marcelo De Oliveira the test of these systems Johann, Ricardo Reis, 2011-08-22 This book contains extended and revised versions of the best papers presented at the 17th IFIP WG 10 5 IEEE International Conference on Very Large Scale Integration VLSI SoC 2009 held in Florian polis Brazil in October 2009 The 8 papers included in the book together with two keynote talks were carefully reviewed and selected from 27 papers presented at the conference The papers cover a wide variety of excellence in VLSI technology and advanced research addressing the current trend toward increasing chip integration and technology process advancements bringing about stimulating new challenges both at the physical and system design levels as well as in the test of theses systems

VLSI-SoC: Forward-Looking Trends in IC and Systems Design Jose L. Ayala, David Atienza Alonso, Ricardo Reis, 2014-04-13 This book contains extended and revised versions of the best papers presented at the 18th IFIP WG 10 5 IEEE International Conference on Very Large Scale Integration VLSI SoC 2010 held in Madrid Spain in September 2010 The 14 papers included in the book were carefully reviewed and selected from the 52 full papers presented at the conference The papers cover a wide variety of excellence in VLSI technology and advanced research They address the current trend toward increasing chip integration and technology process advancements bringing about stimulating new challenges both at the physical and system design levels as well as in the test of theses systems

VLSI-SoC: Opportunities and Challenges Beyond the Internet of Things Michail Maniatakos, Ibrahim (Abe) M. Elfadel, Matteo Sonza Reorda, H. Fatih Ugurdag, José

Monteiro,Ricardo Reis,2019-05-16 This book contains extended and revised versions of the best papers presented at the 25th IFIP WG 10 5 IEEE International Conference on Very Large Scale Integration VLSI SoC 2017 held in Abu Dhabi United Arab Emirates in August 2017 The 11 papers included in this book were carefully reviewed and selected from the 33 full papers presented at the conference The papers cover a wide range of topics in VLSI technology and advanced research They address the latest scientific and industrial results and developments as well as future trends in the field of System on Chip SoC Design On the occasion of the silver jubilee of the VLSI SoC conference series the book also includes a special chapter that presents the history of the VLSI SoC series of conferences and its relation with VLSI SoC evolution since the early 80s up to the present
VLSI-SoC Jürgen Becker,2011
VLSI-SoC: Technology Advancement on SoC Design Elena-Ioana Vatajelu,2025-12-05 This book IFIP 744 contains revised versions of the best papers presented at the 32nd IFIP IEEE International Conference on Very Large Scale Integration System on a Chip VLSI SoC 2024 held in Tangier Morocco during October 6 9 2024 The 8 full papers were carefully reviewed and selected from 65 submissions They were categorized under the topical sections as follows Efficiency and Performance Memory and Data Management Reliability and Trustworthiness

Very Large Scale Integration (VLSI-SoC), 2014 22nd International Conference on ,2014 *VLSI-SoC: Design Trends* Andrea Calimera, Pierre-Emmanuel Gaillardon, Kunal Korgaonkar, Shahar Kvatinsky, Ricardo Reis, 2021 This book contains extended and revised versions of the best papers presented at the 28th IFIP WG 10 5 IEEE International Conference on Very Large Scale Integration VLSI SoC 2020 held in Salt Lake City UT USA in October 2020 The 16 full papers included in this volume were carefully reviewed and selected from the 38 papers out of 74 submissions presented at the conference The papers discuss the latest academic and industrial results and developments as well as future trends in the field of System on Chip SoC design considering the challenges of nano scale state of the art and emerging manufacturing technologies In particular they address cutting edge research fields like low power design of RF analog and mixed signal circuits EDA tools for the synthesis and verification of heterogenous SoCs accelerators for cryptography and deep learning and on chip Interconnection system reliability and testing and integration of 3D ICs The conference was held virtually VLSI-SoC: Internet of Things Foundations Luc Claesen, Maria-Teresa Sanz-Pascual, Ricardo Reis, Arturo Sarmiento-Reyes, 2015 This book contains extended and revised versions of the best papers presented at the 22nd IFIP WG 10 5 IEEE International Conference on Very Large Scale Integration VLSI SoC 2014 held in Playa del Carmen Mexico in October 2014 The 12 papers included in the book were carefully reviewed and selected from the 33 full papers presented at the conference The papers cover a wide range of topics in VLSI technology and advanced research They address the current trend toward increasing chip integration and technology process advancements bringing about stimulating new challenges both at the physical and system design levels as well as in the test of these systems **VLSI-Soc** Andreas Burg, Ayse Coskun, Matthew Guthaus, 2013-11-30

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