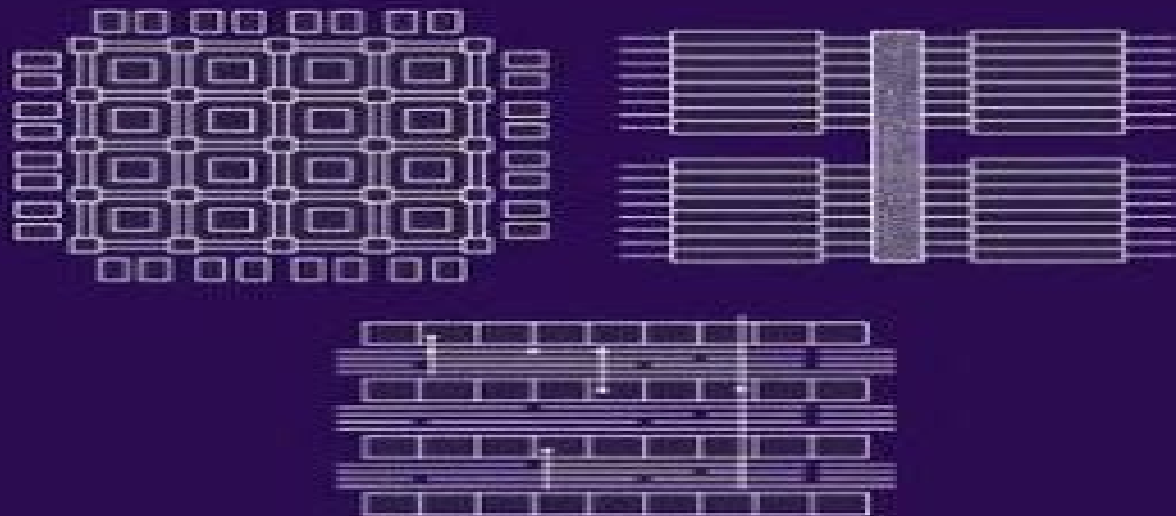


FIELD-PROGRAMMABLE GATE ARRAY TECHNOLOGY



edited by
Stephen M. Trimberger

Field Programmable Gate Array Technology S Trimberger

Hideharu Amano



Field Programmable Gate Array Technology S Trimberger:

Field-Programmable Gate Array Technology Stephen M. Trimberger, Stephen Trimberger, 1994-01-31 Many different kinds of FPGAs exist with different programming technologies different architectures and different software Field Programmable Gate Array Technology describes the major FPGA architectures available today covering the three programming technologies that are in use and the major architectures built on those programming technologies The reader is introduced to concepts relevant to the entire field of FPGAs using popular devices as examples Field Programmable Gate Array Technology includes discussions of FPGA integrated circuit manufacturing circuit design and logic design It describes the way logic and interconnect are implemented in various kinds of FPGAs It covers particular problems with design for FPGAs and future possibilities for new architectures and software This book compares CAD for FPGAs with CAD for traditional gate arrays It describes algorithms for placement routing and optimization of FPGAs Field Programmable Gate Array Technology describes all aspects of FPGA design and development For this reason it covers a significant amount of material Each section is clearly explained to readers who are assumed to have general technical expertise in digital design and design tools Potential developers of FPGAs will benefit primarily from the FPGA architecture and software discussion Electronics systems designers and ASIC users will find a background to different types of FPGAs and applications of their use

Field-Programmable Gate Array Technology Trimberger, 2007-01-01 **The Electronics Handbook** Jerry C. Whitaker, 1996-12-23 The superb organization of The Electronics Handbook means that it is not only a comprehensive and fascinating reference but also a pleasure to use Some of these organizational features include *Handbook of Algorithms for Physical Design Automation* Charles J. Alpert, Dinesh P. Mehta, Sachin S. Sapatnekar, 2008-11-12 The physical design flow of any project depends upon the size of the design the technology the number of designers the clock frequency and the time to do the design As technology advances and design styles change physical design flows are constantly reinvented as traditional phases are removed and new ones are added to accommodate changes in technology Handbook of Algorithms for Physical Design Automation provides a detailed overview of VLSI physical design automation emphasizing state of the art techniques trends and improvements that have emerged during the previous decade After a brief introduction to the modern physical design problem basic algorithmic techniques and partitioning the book discusses significant advances in floorplanning representations and describes recent formulations of the floorplanning problem The text also addresses issues of placement net layout and optimization routing multiple signal nets manufacturability physical synthesis special nets and designing for specialized technologies It includes a personal perspective from Ralph Otten as he looks back on the major technical milestones in the history of physical design automation Although several books on this topic are currently available most are either too broad or out of date Alternatively proceedings and journal articles are valuable resources for researchers in this area but the material is widely dispersed in the literature This handbook pulls together a broad variety of perspectives

on the most challenging problems in the field and focuses on emerging problems and research results

FPGA Design Automation Deming Chen, Jason Cong, Peichen Pan, 2006 FPGA Design Automation A Survey is an up to date comprehensive survey tutorial of FPGA design automation with an emphasis on the recent developments within the past 5 to 10 years The focus is on the theory and techniques that have been or most likely will be reduced to practice It covers all major steps in FPGA design flow routing and placement circuit clustering technology mapping and architecture specific optimization physical synthesis RT level and behavior level synthesis and power optimization FPGA Design Automation A Survey can be used as both a guide for beginners who are embarking on research in this relatively young yet exciting area and a useful reference for established researchers in this field

Principles and Structures of FPGAs Hideharu Amano, 2018-09-03 This comprehensive textbook on the field programmable gate array FPGA covers its history fundamental knowledge architectures device technologies computer aided design technologies design tools examples of application and future trends Programmable logic devices represented by FPGAs have been rapidly developed in recent years and have become key electronic devices used in most IT products This book provides both complete introductions suitable for students and beginners and high level techniques useful for engineers and researchers in this field Differently developed from usual integrated circuits the FPGA has unique structures design methodologies and application techniques Allowing programming by users the device can dramatically reduce the rising cost of development in advanced semiconductor chips The FPGA is now driving the most advanced semiconductor processes and is an all in one platform combining memory CPUs and various peripheral interfaces This book introduces the FPGA from various aspects for readers of different levels Novice learners can acquire a fundamental knowledge of the FPGA including its history from Chapter 1 the first half of Chapter 2 and Chapter 4 Professionals who are already familiar with the device will gain a deeper understanding of the structures and design methodologies from Chapters 3 and 5 Chapters 6 8 also provide advanced techniques and cutting edge applications and trends useful for professionals Although the first parts are mainly suitable for students the advanced sections of the book will be valuable for professionals in acquiring an in depth understanding of the FPGA to maximize the performance of the device

Low-Energy FPGAs — Architecture and Design Varghese George, Jan M. Rabaey, 2012-12-06 Low Energy FPGAs Architecture and Design is a primary resource for both researchers and practicing engineers in the field of digital circuit design The book addresses the energy consumption of Field Programmable Gate Arrays FPGAs FPGAs are becoming popular as embedded components in computing platforms The programmability of the FPGA can be used to customize implementations of functions on an application basis This leads to performance gains and enables reuse of expensive silicon Chapter 1 provides an overview of digital circuit design and FPGAs Chapter 2 looks at the implication of deep submicron technology on FPGA power dissipation Chapter 3 describes the exploration environment to guide and evaluate design decisions Chapter 4 discusses the architectural optimization process to evaluate the trade offs between the flexibility of the

architecture and the effect on the performance metrics Chapter 5 reviews different circuit techniques to reduce the performance overhead of some of the dominant components Chapter 6 shows methods to configure FPGAs to minimize the programming overhead Chapter 7 addresses the physical realization of some of the critical components and the final implementation of a specific low energy FPGA Chapter 8 compares the prototype array to an equivalent commercial architecture

Synthesis Techniques and Optimizations for Reconfigurable Systems Ryan Kastner,Adam Kaplan,Majid Sarrafzadeh,2003-10-27 Synthesis Techniques and Optimization for Reconfigurable Systems discusses methods used to model reconfigurable applications at the system level many of which could be incorporated directly into modern compilers The book also discusses a framework for reconfigurable system synthesis which bridges the gap between application level compiler analysis and high level device synthesis The development of this framework discussed in Chapter 5 and the creation of application analysis which further optimize its output discussed in Chapters 7 8 and 9 represent over four years of rigorous investigation within UCLA s Embedded and Reconfigurable Laboratory ERLab and UCSB s Extensible Programmable and Reconfigurable Embedded SystemS ExPRESS Group The research of these systems has not yet matured and we continually strive to develop data and methods which will extend the collective understanding of reconfigurable system synthesis

Fine- and Coarse-Grain Reconfigurable Computing Stamatis Vassiliadis,Dimitrios Soudris,2007-09-24 Fine and Coarse Grain Reconfigurable Computing gives the basic concepts and building blocks for the design of Fine or FPGA and Coarse Grain Reconfigurable Architectures Recently developed integrated architecture design and software supported design flow of FPGA and coarse grain reconfigurable architecture are also described Part I consists of two extensive surveys of FPGA and Coarse Grain Reconfigurable Architectures In Part II case studies innovative research results about reconfigurable architectures and design frameworks from three projects AMDREL MOLEN and ADRES and DRESC and a new classification according to microcoded architectural criteria are described Fine and Coarse Grain Reconfigurable Computing is an essential reference for researchers and professionals and can be used as a textbook by undergraduate graduate students and professors

FPGA ... ,2005 **Field-programmable Logic and Applications** ,2000

Field-Programmable Gate Arrays Stephen D. Brown,Robert J. Francis,Jonathan Rose,Zvonko G. Vranesic,2012-12-06 Field Programmable Gate Arrays FPGAs have emerged as an attractive means of implementing logic circuits providing instant manufacturing turnaround and negligible prototype costs They hold the promise of replacing much of the VLSI market now held by mask programmed gate arrays FPGAs offer an affordable solution for customized VLSI over a wide variety of applications and have also opened up new possibilities in designing reconfigurable digital systems Field Programmable Gate Arrays discusses the most important aspects of FPGAs in a textbook manner It provides the reader with a focused view of the key issues using a consistent notation and style of presentation It provides detailed descriptions of commercially available FPGAs and an in depth treatment of the FPGA architecture and CAD issues that are the subjects of

current research The material presented is of interest to a variety of readers including those who are not familiar with FPGA technology but wish to be introduced to it as well as those who already have an understanding of FPGAs but who are interested in learning about the research directions that are of current interest

Microsystems Technology for Multimedia Applications Bing Jay Sheu, 1995 Current interest in NAD Nicotinamide adenine dinucleotide in biological systems focuses on its role in ADP ribose transfer reactions These appear to be fundamentally involved in the regulation of many physiological processes The contributions in this monograph thus represent the range of research in the very active investigation of niacin metabolism The major topics covered are Enzymology of ADP Ribosylation ADP Ribosylation and Chromatin Function Carcinogenesis and Differentiation NAD Metabolism and Chemotherapy ADP Ribosylation and Signal Transduction Molecular Genetic Approaches to

Advanced Research in VLSI and Parallel Systems Thomas Knight, John E. Savage, 1992 The design of highly integrated or large scale systems involves a set of interrelated disciplines including circuits and devices design automation VLSI architecture software systems and theory

Architecture and CAD for Deep-Submicron FPGAs Vaughn Betz, Jonathan Rose, Alexander Marquardt, 1999-03-31 Architecture and CAD for Deep Submicron FPGAs addresses several key issues in the design of high performance FPGA architectures and CAD tools with particular emphasis on issues that are important for FPGAs implemented in deep submicron processes Three factors combine to determine the performance of an FPGA the quality of the CAD tools used to map circuits into the FPGA the quality of the FPGA architecture and the electrical i e transistor level design of the FPGA Architecture and CAD for Deep Submicron FPGAs examines all three of these issues in concert

Proceedings , 1994

Field-Programmable Gate Arrays John V. Oldfield, Richard C. Dorf, 1995-01-23 Due to unique advantages like security improved testing and reprogrammability field programmable gate arrays are making broad inroads in the electronics industry This comprehensive overview of the topic explains the underlying principles strengths and limitations of a range of FPGA architectures Includes abundant references and illustrations

EURO-DAC ... , 1995 Euro-DAC '95, European Design Automation Conference with Euro-VHDL , 1995

Novel Techniques for High Performance Field Programmable Logic Devices Narasimha B. Bhat, 1993

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