

FUNDAMENTALS OF ELECTRIC CIRCUIT ANALYSIS

CLAYTON PAUL

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Fundamentals of Electric Circuit Analysis Clayton R. Paul, 2001 Focusing on the development of fundamental skills this new text is designed for a one semester course in the analysis of linear circuits. The author meticulously covers the important topics within a sound pedagogical organization while minimizing unnecessary detail so that the student can develop a lasting and sound set of analysis skills The major topics presented include the analysis of resistive circuits including controlled sources and op amps and the analysis of circuits in the sinusoidal steady state phasor analysis Emphasized also is the analysis of circuits in the time domain in response to a disturbance switching operations and the unit step and unit impulse responses and is developed primarily using the Laplace transform A brief description of the classical method of solving the circuit differential equations is included Transmission Lines in Digital Systems for EMC Practitioners Clayton R. Paul, 2011-10-24 This is a brief but comprehensive book covering the set of EMC skills that EMC practitioners today require in order to be successful in high speed digital electronics. The basic skills in the book are new and weren t studied in most curricula some ten years ago The rapidly changing digital technology has created this demand for a discussion of new analysis skills particularly for the analysis of transmission lines where the conductors that interconnect the electronic modules have become electrically large longer than a tenth of a wavelength which are increasingly becoming important Crosstalk between the lines is also rapidly becoming a significant problem in getting modern electronic systems to work satisfactorily Hence this text concentrates on the modeling of electrically large connection conductors where previously used Kirchhoff's voltage and current laws and lumped circuit modeling have become obsolete because of the increasing speeds of modern digital systems This has caused an increased emphasis on Signal Integrity Until as recently as some ten years ago digital system clock speeds and data rates were in the hundreds of megahertz MHz range Prior to that time the lands on printed circuit boards PCBs that interconnect the electronic modules had little or no impact on the proper functioning of those electronic circuits Today the clock and data speeds have moved into the low gigahertz GHz range

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