

ACSP - Analog Circuits and Signal Processing

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Distributed CMOS Bidirectional Amplifiers

Broadbanding and Linearization
Techniques



Springer

Distributed Cmos Bidirectional Amplifiers

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Distributed Cmos Bidirectional Amplifiers:

Distributed CMOS Bidirectional Amplifiers Ziad El-Khatib, Leonard MacEachern, Samy A. Mahmoud, 2012-05-02 This book describes methods to design distributed amplifiers useful for performing circuit functions such as duplexing paraphrase amplification phase shifting power splitting and power combiner applications A CMOS bidirectional distributed amplifier is presented that combines for the first time device level with circuit level linearization suppressing the third order intermodulation distortion It is implemented in 0.13um RF CMOS technology for use in highly linear low cost UWB Radio over Fiber communication systems

Highly-linearized CMOS Distributed Bidirectional Amplifier with Cross-coupled Compensator for Wireless Communications Ziad El-Khatib, Carleton University. Dissertation. Engineering, Electrical and Computer, 2012

Recent Advances in Satellite Aeronautical Communications Modeling Grekhov, Andrii Mikhailovich, 2019-03-15 Modern systems and means of aeronautical radio communication are continuously being improved but without the development of new technical means the aviation industry suffers The development of more innovative plans of aviation technology are needed in order to respond to the ever increasing standard of aviation technology Recent Advances in Satellite Aeronautical Communications Modeling is devoted to the modeling of satellite communication channels for aircraft and RPAS UAV using the Matlab Simulink and NetCracker software Featuring research on topics such as channel coding microwave emitters and array modeling this book is ideally designed for scientists engineers air traffic controllers managers researchers and academicians

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IEEE Transactions on Circuits and Systems, 2006 Silicon-Based High-Sensitivity Broadband Receiver Xiaojun Bi, 2024-03-19 This book presents various design theories and methodologies for silicon based high sensitivity broadband receivers including millimeter wave radiometer chips and photoelectric receivers which are core elements in imaging systems data centers and telecommunication infrastructures As a key module in application systems the high sensitivity broadband receiver not only attracts the attention of engineers and researchers in the radio frequency and optoelectronic fields but also garners significant interest from other disciplines including optics

communications and security The book introduces various silicon based critical design technologies aim to overcome the limitations inherent in silicon devices distinctly enhancing sensitivity with a broad bandwidth These innovative design methodologies initially proposed and subsequently validated through meticulous measurements represent a pioneering contribution The book provides readers with detailed insights into design intricacies and considerations Its audience includes undergraduate and graduate students with a specific interest in RF optoelectronic receiver technology along with researchers and engineers engaged in the study of imaging systems data centers or other communication applications

Design of CMOS Distributed Amplifiers for Broadband Wireline and Wireless Communication Applications

Kambiz Khodayari Moez, 2006 While the RF building blocks of narrowband system on chip designs have increasingly been created in CMOS during the past decade researchers have started to look at the possibility of implementation of broadband transceivers in CMOS technology High speed optical links with operating frequencies of up to 40 GHz and ultra wideband UWB wireless systems operating in 3 to 10 GHz frequency band are examples of these broadband applications CMOS offers a low fabrication cost and a higher level of integration compared with compound semiconductor technologies that currently claim broadband RFIC applications In this work we focus on the design of broadband low noise amplifiers the fundamental building blocks of high data rate wireline and wireless telecommunication systems A well established microwave engineering technique distributed amplification with a potential bandwidth up to the cut off frequency of transistors is employed However the implementation of distributed amplifiers in CMOS imposes new challenges such as gain attenuation because of substrate loss of on chip inductors a typical large die area and a large noise figure These problems have been addressed in this dissertation as described below On chip inductors the essential components of the distributed amplifiers gate and drain transmission lines dissipate more and more power in silicon substrates as well as in metal lines as frequency increases which in turn reduces the gain and deteriorates the input output matching Using active negative resistors implemented by a capacitively source degenerated configuration we have fully compensated the loss of the transmission lines in order to achieve a flat gain of 10 dB over the entire DC to 44 GHz bandwidth We have addressed another drawback of distributed amplifiers large die area by utilizing closely placed RF transmission lines instead of spiral inductors Because of a more compact implementation of transmission lines the area of the distributed amplifiers is considerably reduced at the expense of extra design steps required for the modeling of the closely placed RF transmission lines A post layout simulation method is developed to take into account the effect of inductive and capacitive coupling by incorporating a 3D EM simulator into the design process A 9 dB 27 GHz distributed amplifier has been fabricated in an area as small as 0.17 mm² using 180nm TSMC s CMOS process For wireless applications UWB a very low noise figure is required for the broadband preamplifier Conventional distributed amplifiers fail to provide a low noise figure mainly because of the noise injected by the terminating resistor of the gate transmission lines We have replaced the terminating resistor with a frequency dependent resistor which

trades off the low frequency input matching of the distributed amplifier not required for UWB with a better noise performance Our proposed design provides a gain of 12 dB with an average noise figure of 3.4 dB over the entire 3.10 GHz band advancing the state of the art implementation of broadband LNAs

Communication Architectures for Systems-on-Chip José L. Ayala, 2018-09-03 A presentation of state of the art approaches from an industrial applications perspective Communication Architectures for Systems on Chip shows professionals researchers and students how to attack the problem of data communication in the manufacture of SoC architectures With its lucid illustration of current trends and research improving the performance quality and reliability of transactions this is an essential reference for anyone dealing with communication mechanisms for embedded systems systems on chip and multiprocessor architectures or trying to overcome existing limitations Exploring architectures currently implemented in manufactured SoCs and those being proposed this book analyzes a wide range of applications including Well established communication buses Less common networks on chip Modern technologies that include the use of carbon nanotubes CNTs Optical links used to speed up data transfer and boost both security and quality of service QoS The book's contributors pay special attention to newer problems including how to protect transactions of critical on chip information personal data security keys etc from an external attack They examine mechanisms revise communication protocols involved and analyze overall impact on system performance

0.1-8 GHz CMOS Distributed Amplifier Ali Ekber Kılıç, Metin Yazgı (Danışman.), Elektrik-Elektronik Fakültesi, 2007

Intelligent Systems Design and Applications Ajith Abraham, Anu Bajaj, Thomas Hanne, 2024-07-22 This book highlights recent research on intelligent systems and machine learning based solutions It presents 46 selected papers focused on Industrial Applications from the 23rd International Conference on Intelligent Systems Design and Applications ISDA 2023 which was held in 5 different cities namely Olten Switzerland Porto Portugal Kaunas Lithuania Greater Noida India Kochi India and in online mode The ISDA is a premier conference in the field of artificial intelligence and the latest installment brought together researchers engineers and practitioners whose work involves intelligent systems and their applications in industry ISDA 2023 had contributions by authors from 64 countries This book offers a valuable reference guide for all industrial specialists scientists academicians researchers students and practitioners in the field of artificial intelligence and industrial applications

Electrical & Electronics Abstracts, 1997

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Design of High Gain-bandwidth

Distributed Amplifiers in CMOS Technology Mohammad Amin Arbabian, 2007 *Distributed Power Amplifiers for RF and Microwave Communications* Narendra Kumar, Andrei Grebennikov, 2015-06-01 This new resource presents readers with all relevant information and comprehensive design methodology of wideband amplifiers This book specifically focuses on distributed amplifiers and their main components and presents numerous RF and microwave applications including well known historical and recent architectures theoretical approaches circuit simulation and practical implementation techniques A great resource for practicing designers and engineers this book contains numerous well known and novel practical circuits architectures and theoretical approaches with detailed description of their operational principles Fundamentals of Distributed Amplification Thomas Tang Yum Wong, 1993 The first book on this important growing technology covers basic principles of distributed amplification and their most important derived results Features 500 equations and 102 illustrations

Solid-State Sensor and Actuator Workshop, Hilton Head Island, South Carolina, June 3-6, 1996, 1996 **JJAP Letters**, 2002 **Index to IEEE Publications** Institute of Electrical and Electronics Engineers, 1997 Japanese Journal of Applied Physics, 1998 *Bandwidth and Gain Extension Technique for CMOS Distributed Amplifiers Using Negative Capacitance and Resistance Cell*, 2017

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