FUNDAMENTALS OF SEMICONDUCTOR PROCESSING TECHNOLOGIES

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Fundamentals of Semiconductor Processing Technology Badih El-Kareh,Lou N. Hutter,2012-12-06 The drive toward new semiconductor technologies is intricately related to market demands for cheaper smaller faster and more reliable circuits with lower power consumption The development of new processing tools and technologies is aimed at optimizing one or more of these requirements This goal can however only be achieved by a concerted effort between scientists engineers technicians and operators in research development and manufac turing It is therefore important that experts in specific disciplines such as device and circuit design understand the principle capabil ities and limitations of tools and processing technologies It is also important that those working on specific unit processes such as lithography or hot processes be familiar with other unit processes used to manufacture the product Several excellent books have been published on the subject of process technologies These texts however cover subjects in too much detail or do not cover topics important to modem tech nologies This book is written with the need for a bridge between different disciplines in mind It is intended to present to engineers and scientists those parts of modem processing technologies that are of greatest importance to the design and manufacture of semi conductor circuits The material is presented with sufficient detail to understand and analyze interactions between processing and other semiconductor disciplines such as design of devices and cir cuits their electrical parameters reliability and yield Fundamentals of Semiconductor Manufacturing and Process Control Gary S. May,Costas J. Spanos,2006-05-26 A practical guide to semiconductor manufacturing from processcontrol to yield modeling and experimental design Fundamentals of Semiconductor Manufacturing and Process Controlcovers all issues involved in manufacturing microelectronic devicesand circuits including fabrication sequences process control experimental design process modeling yield modeling and CIM CAMsystems Readers are introduced to both the theory and practice ofall basic manufacturing concepts Following an overview of manufacturing and technology the textexplores process monitoring methods including those that focus onproduct wafers and those that focus on the equipment used toproduce wafers Next the text sets forth some fundamentals ofstatistics and yield modeling which set the foundation for adetailed discussion of how statistical process control is used toanalyze quality and improve yields The discussion of statistical experimental design offers readers apowerful approach for systematically varying controllable processconditions and determining their impact on output parameters thatmeasure quality The authors introduce process modeling concepts including several advanced process control topics such asrun by run supervisory control and process and equipmentdiagnosis Critical coverage includes the following Combines process control and semiconductor manufacturing Unique treatment of system and software technology and managementof overall manufacturing systems Chapters include case studies sample problems and suggestedexercises Instructor support includes electronic copies of the figures andan instructor s manual Graduate level students and industrial

practitioners will benefitfrom the detailed exami nation of how electronic materials andsupplies are converted into finished integrated circuits andelectronic products in a high volume manufacturingenvironment An Instructor s Manual presenting detailed solutions to all theproblems in the book is available from the Wiley editorialdepartment An Instructor Support FTP site is also available *Crucial Issues in Semiconductor Materials and Processing Technologies* S. Coffa,F. Priolo,Emanuele Rimini,J.M. Poate,2012-12-06 Semiconductors lie at the heart of some of the most important industries and technologies of the twentieth century The complexity of silicon integrated circuits is increasing considerably because of the continuous dimensional shrinkage to improve efficiency and functionality This evolution in design rules poses real challenges for the materials scientists and processing engineers Materials defects and processing now have to be understood in their totality World experts discuss in this volume the crucial issues facing lithography ion implication and plasma processing metallization and insulating layer quality and crystal growth Particular emphasis is placed upon silicon but compound semiconductors and photonic materials are also highlighted The fundamental concepts of phase stability interfaces and defects play a key role in understanding these crucial issues These concepts are reviewed in a crucial fashion

DeGarmo's Materials and Processes in Manufacturing Ernest Paul DeGarmo,J. T. Black,Ronald A. Kohser,2011-08-30 Now in its eleventh edition DeGarmo s Materials and Processes in Manufacturing has been a market leading text on manufacturing and manufacturing processes courses for more than fifty years Authors J T Black and Ron Kohser have continued this book s long and distinguished tradition of exceedingly clear presentation and highly practical approach to materials and processes presenting mathematical models and analytical equations only when they enhance the basic understanding of the material Completely revised and updated to reflect all current practices standards and materials the eleventh edition has new coverage of additive manufacturing lean engineering and processes related to ceramics polymers and plastics **Handbook of Semiconductor Technology, Volume 2** Kenneth A. Jackson,Wolfgang Schröter,2000-08-15 Semiconductor technology is the basis of today s microelectronics industry with its many impacts on our modern life i e computer and communication technology This two volume handbook covers the basics of semiconductor processing technology which are as essential for the design of new microelectronic devices as the fundamental physics Volume 1 Electronic Structure and Properties covers the structure and properties of semiconductors with particular emphasis on concepts relevant to semiconductor technology Volume 2 Processing of Semiconductors deals with the enabling materials technology for the electronics industry World renowned authors have contributed to this unique treatment of the processing of semiconductors and related technologies Of interest to physicists and engineers in research and in the electronics industry this is a valuable reference source and state of the art review by the world s top authors **Predictive Simulation of Semiconductor Processing** Jarek Dabrowski,Eicke R. Weber,2013-03-09 Predictive Simulation of Semiconductor Processing enables researchers and developers to extend the scaling range of semiconductor devices beyond

the parameter range of empirical research It requires a thorough understanding of the basic mechanisms employed in device fabrication such as diffusion ion implantation epitaxy defect formation and annealing and contamination This book presents an in depth discussion of our current understanding of key processes and identifies areas that require further work in order to achieve the goal of a comprehensive predictive process simulation tool **Sputtering Materials for VLSI and Thin Film Devices** Jaydeep Sarkar,2010-12-13 An important resource for students engineers and researchers working in the area of thin film deposition using physical vapor deposition e g sputtering for semiconductor liquid crystal displays high density recording media and photovoltaic device e g thin film solar cell manufacturing This book also reviews microelectronics industry topics such as history of inventions and technology trends recent developments in sputtering technologies manufacturing steps that require sputtering of thin films the properties of thin films and the role of sputtering target performance on overall productivity of various processes Two unique chapters of this book deal with productivity and troubleshooting issues The content of the book has been divided into two sections a the first section Chapter 1 to Chapter 3 has been prepared for the readers from a range of disciplines e g electrical chemical chemistry physics trying to get an insight into use of sputtered films in various devices e g semiconductor display photovoltaic data storage basic of sputtering and performance of sputtering target in relation to productivity and b the second section Chapter 4 to Chapter 8 has been prepared for readers who already have background knowledge of sputter deposition of thin films materials science principles and interested in the details of sputtering target manufacturing methods sputtering behavior and thin film properties specific to semiconductor liquid crystal display photovoltaic and magnetic data storage applications In Chapters 5 to 8 a general structure has been used i e a description of the applications of sputtered thin films sputtering target manufacturing methods including flow charts sputtering behavior of targets e g current voltage relationship deposition rate and thin film properties e g microstructure stresses electrical properties in film particles While discussing these topics attempts have been made to include examples from the actual commercial processes to highlight the increased complexity of the commercial processes with the growth of advanced technologies In addition to personnel working in industry setting university researchers with advanced knowledge of sputtering would also find discussion of such topics e g attributes of target design chamber design target microstructure sputter surface characteristics various troubleshooting issues useful Unique coverage of sputtering target manufacturing methods in the light of semiconductor displays data storage and photovoltaic industry requirements Practical information on technology trends role of sputtering and major OEMs Discussion on properties of a wide variety of thin films which include silicides conductors diffusion barriers transparent conducting oxides magnetic films etc Practical case studies on target performance and troubleshooting Essential technological information for students engineers and scientists working in the semiconductor display data storage and photovoltaic industry *Crystal Growth and Evaluation of Silicon for VLSI and ULSI* Golla Eranna,2014-12-08 Silicon as a single crystal semiconductor has sparked a revolution in the

field of electronics and touched nearly every field of science and technology Though available abundantly as silica and in various other forms in nature silicon is difficult to separate from its chemical compounds because of its reactivity As a solid silicon is chemically inert and stable but growing it as a single crystal creates many technological challenges Crystal Growth and Evaluation of Silicon for VLSI and ULSI is one of the first books to cover the systematic growth of silicon single crystals and the complete evaluation of silicon from sand to useful wafers for device fabrication Written for engineers and researchers working in semiconductor fabrication industries this practical text Describes different techniques used to grow silicon single crystals Explains how grown single crystal ingots become a complete silicon wafer for integrated circuit fabrication Reviews different methods to evaluate silicon wafers to determine suitability for device applications Analyzes silicon wafers in terms of resistivity and impurity concentration mapping Examines the effect of intentional and unintentional impurities Explores the defects found in regular silicon crystal lattice Discusses silicon wafer preparation for VLSI and ULSI processing Crystal Growth and Evaluation of Silicon for VLSI and ULSI is an essential reference for different approaches to the selection of the basic silicon containing compound separation of silicon as metallurgical grade pure silicon subsequent purification single crystal growth and defects and evaluation of the deviations within the grown crystals **Encyclopedia of Physical Science and Technology** ,2002 Of the Encyclopedia of Physical Science and Technology Has been completely updated with no less than 90% revised material and 50% new content throughout the volumes Presents eighteen volumes nearly 800 authoritative articles and 14 500 pages Is lavishly illustrated with over 7 000 photographs illustrations and tables Presents an increased emphasis on the hottest topics such as information processing environmental science biotechnology and biomedicine Includes a final Index Volume containing Thematic Relational and Subject indexes **The Cumulative Book Index** ,1996 A world list of books in the English language *III-Mn-V Ferromagnetic Semiconductors Synthesized by Ion Implantations and Pulsed-laser Melting* Michael Andrew Scarpulla,2006 Latchup Steven H. Voldman,2008-04-15 Interest in latchup is being renewed with the evolution of complimentary metal oxide semiconductor CMOS technology metal oxide semiconductor field effect transistor MOSFET scaling and high level system on chip SOC integration Clear methodologies that grant protection from latchup with insight into the physics technology and circuit issues involved are in increasing demand This book describes CMOS and BiCMOS semiconductor technology and their sensitivity to present day latchup phenomena from basic over voltage and over current conditions single event latchup SEL and cable discharge events CDE to latchup domino phenomena It contains chapters focusing on bipolar physics latchup theory latchup and guard ring characterization structures characterization testing product level test systems product level testing and experimental results Discussions on state of the art semiconductor processes design layout and circuit level and system level latchup solutions are also included as well as latchup semiconductor process solutions for both CMOS to BiCMOS such as shallow trench deep trench retrograde wells connecting implants sub collectors heavily doped buried layers and buried grids from single to triple

well CMOS practical latchup design methods automated and bench level latchup testing methods and techniques latchup theory of logarithm resistance space generalized alpha a space beta b space new latchup design methods connecting the theoretical to the practical analysis and examples of latchup computer aided design CAD methodologies from design rule checking DRC and logical to physical design to new latchup CAD methodologies that address latchup for internal and external latchup on a local as well as global design level Latchup acts as a companion text to the author s series of books on ESD electrostatic discharge protection serving as an invaluable reference for the professional semiconductor chip and system level ESD engineer Semiconductor device process and circuit designers and quality reliability and failure analysis engineers will find it informative on the issues that confront modern CMOS technology Practitioners in the automotive and aerospace industries will also find it useful In addition its academic treatment will appeal to both senior and graduate students with interests in semiconductor process device physics computer aided design and design integration **Mixed-Signal Systems** Andrzej Handkiewicz,2002-08-08 A practical guide to the successful integration of digital and analog circuits Mixed signal processing the integration of digital and analog circuitry within computer systems enables systems to take signals from the analog world and process them within a digital system In fact recent advances in VLSI technology performance now allow for the integration of digital and analog circuits on a single chip a process that requires the use of analog pre and post processing systems such as converters filters sensors drivers buffers and actuators However the lack of universal CAD tools for the synthesis simulation and layout of the analog part of the chip represents a design bottleneck of today s VLSI circuits Mixed Signal Systems A Guide to CMOS Circuit Design presents a comprehensive general overview of the latest CMOS technology and covers the various computer systems that may be used for designing integrated circuits Taking an original approach to one and two dimensional filter design the author explores the many digital oriented design systems or silicon compilers currently being used and presents the basic methods procedures and tools used by each In a thorough and systematic manner the text Presents common features of digital oriented design systems Describes methods and tools that are not yet being applied in any compiler Illustrates image processing systems that can be implemented on a single chip Demonstrates the path from synthesis methods to the actual silicon assembly Essential reading for integrated circuit designers and developers of related computer programs as well as advanced students of system design this book represents an invaluable resource for anyone involved in the development of mixed signal systems **Microchip Fabrication: A Practical Guide to Semiconductor Processing, Sixth Edition** Peter Van Zant,2013-10-22 The most complete current guide to semiconductor processing Fully revised to cover the latest advances in the field Microchip Fabrication Sixth Edition explains every stage of semiconductor processing from raw material preparation to testing to packaging and shipping the finished device This practical resource provides easy to understand information on the physics chemistry and electronic fundamentals underlying the sophisticated manufacturing materials and processes of modern semiconductors State of the art

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